## JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for II B.Tech I Semester (R16) Regular/Supplementary Examinations Dec-2018 College name: JNTU KAKINADA UNIVERSITY COLLEGE OF ENGINEERING, VIZIANAGARAM:VV

Htno	Subcode	Subname	Grade	Credits
15VV1A0210	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	D	3
15VV1A0210	R1621022	ELECTRICAL MACHINES-I	F	0
16VV1A0114	R1621011	PROBABILITY & STATISTICS C		3
16VV1A0207	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI B		3
16VV1A0219	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II		0
16VV1A0219	R1621023	BASIC ELECTRONICS AND DEVICES		0
16VV1A0221	R1621023	BASIC ELECTRONICS AND DEVICES		3
16VV1A0226	R1621028	ELECTRICAL CIRCUITS LABORATORY		2
16VV1A0229	R1621022	ELECTRICAL MACHINES-I		3
16VV1A0229	R1621026	MANAGERIAL ECONOMICS & FINANCIAL ANALYSI		3
16VV1A0251	R1621023	BASIC ELECTRONICS AND DEVICES		3
16VV1A0305	R1621034	FLUID MECHANICS & HYDRAULIC MACHINES		3
16VV1A0312	R1621034	FLUID MECHANICS & HYDRAULIC MACHINES		3
16VV1A0322	R1621034	FLUID MECHANICS & HYDRAULIC MACHINES		3
16VV1A0338	R1621032	MECHANICS OF SOLIDS	F	0
16VV1A0338	R1621034	FLUID MECHANICS & HYDRAULIC MACHINES	D	3
16VV1A0346	R1621033	THERMODYNAMICS	F	0
16VV1A0353	R1621035	COMPUTER AIDED ENGINEERING DRAWING PRACT	В	3
16VV1A0353	R1621037	MECHANICS OF SOLIDS & METALLURGY LAB	С	2
16VV1A0403	R1621042	SWITCHING THEORY AND LOGIC DESIGN	С	3
16VV1A0415	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
16VV1A0447	R1621041	ELECTRONIC DEVICES AND CIRCUITS	F	0
16VV1A0447	R1621043	SIGNALS AND SYSTEMS		0
16VV1A0455	R1621045	RANDOM VARIABLES AND STOCHASTIC PROCESS		0
16VV1A0544	R1621051	STATISTICS WITH R PROGRAMMING	F	0
16VV1A0548	R1621051	STATISTICS WITH R PROGRAMMING	D	3
16VV1A0556	R1621051	STATISTICS WITH R PROGRAMMING	D	3
16VV1A0556	R1621052	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	D	3
16VV1A1202	R1621053	DIGITAL LOGIC DESIGN	F	0
16VV1A1208	R1621051	STATISTICS WITH R PROGRAMMING	С	3
16VV1A1235	R1621053	DIGITAL LOGIC DESIGN	С	3
16VV1A1236	R1621051	STATISTICS WITH R PROGRAMMING	С	3
16VV1A3102	R1621174	MECHANICS OF SOLIDS AND FLUIDS F		0
17VV5A0462	R1621043	SIGNALS AND SYSTEMS	D	3
17VV5A0462	R1621044	NETWORK ANALYSIS	ABSENT	0
17VV5A1262	R1621051	STATISTICS WITH R PROGRAMMING	С	3
17VV5A1263	R1621053	DIGITAL LOGIC DESIGN	С	3
17VV5A1267	R1621053	DIGITAL LOGIC DESIGN	F	0
17VV5A3131	R1621174	MECHANICS OF SOLIDS AND FLUIDS	D	3
17VV5A3137	R1621011	PROBABILITY AND STATISTICS	D	3

Marks Range Theory	Marks Range Lab	Letter Grade	Level	Grade Point
>=90	>=67	0	Outstanding	10
>=80 to <90	>=60 to <67	S	Excellent	9
>=70 to <80	>=52 to <60	А	Very Good	8
>=60 to <70	>=45 to <52	В	Good	7
>=50 to <60	>=37 to <45	С	Fair	6
>=40 to <50	>=30 to <37	D	Satisfactory	5
<40	<30	F	Fail	0
			Absent	0

Date:01-02-2019

Controller of Examinations