

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA
RESULT FOR M.Tech I SEMESTER SPECIAL SUPPLEMENTARY EXAMINATIONS MAY - 2018

SNO	HTNO	SUBCODE	SUBNAME	INTERNALS	EXTERNALS	CREDITS
1	08A71D3802	A3802	ADVANCED DIGITAL SIGNAL PROCESSING	30	2	0
2	10501D1505	C0403	GEOMETRIC MODELING	28	24	1
3	10991D5707	C5503	ANALOG AND DIGITAL IC DESIGN	30	-1	0
4	10991D5707	C5701	ELECTRONIC DESIGN AUTOMATION TOOLS	35	-1	0
5	10A91D5830	C5801	DATASTRUCTURES AND ALGORITHM ANALYSIS	26	13	0

250	13S01D5813	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	33	14	0
251	13S01D5813	G0503	DATA BASE MANAGEMENT SYSTEMS	35	27	1
252	13S01D5813	G0504	OPERATING SYSTEM	30	1	0
253	13S01D5813	G0505	SOFTWARE ENGINEERING	32	31	1
254	13U91D0509	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	22	-1	0
255	13U91D0509	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM ANALYSIS	16	-1	0
256	13U91D2514	G2506	MIDDLEWARE TECHNOLOGIES	24	19	0
257	13U91D5409	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	26	-1	0
258	13U91D7007	G6802	VLSI TECHNOLOGY AND DESIGN	27	21	0
259	13U91D7008	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	35	21	0
260	13VV1D1509	G1502	ADVANCED MECHANICS OF SOLIDS	11	32	0
261	13W31D0509	G0503	DATA BASE MANAGEMENT SYSTEMS	39	18	0
262	13W31D4114	G41103	CONTINUUM MECHANICS	36	4	0
263	13W31D5617	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	39	5	0
264	13W31D5617	G5613	PROGRAMMABLE LOGIC CONTROLLERS & APPLICATIONS	38	2	0
265	13W32D5710	G6803	CMOS ANALOG IC DESIGN	39	27	1
266	13W32D5815	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM ANALYSIS	36	15	0
267	13W61D4311	G5602	HVDC TRANSMISSION	30	3	0
268	13X21D5509	G5501	EMBEDDED SYSTEM DESIGN	37	24	1
269	13X21D5803	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCIENCE	40	-1	0
270	13X21D5803	G0503	DATA BASE MANAGEMENT SYSTEMS	37	-1	0
271	13X41D7010	G8205	DETECTION & ESTIMATION THEORY	21	5	0

LAST DATE FOR RCRV: 04-09-2018

CONTROLLER OF EXAMINATIONS

N. Reddy Rao