Revaluation Results of II B.Tech [R16,R13 & R10] I Semester [VV] Regular/Supplementary Examination NOV-2017

Slno	CC	HTNO	SUBCODE	SUBNAME	GRADE	CREDITS
1	VV	16VV1A0215	R1621023	BASIC ELECTRONICS AND DEVICES	B	3
2	VV	16VV1A0420	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
3	VV	16VV1A0420	R1621043	SIGNALS AND SYSTEMS	C	3
4	VV	16VV1A0422	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
5	VV	16VV1A0430	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
6	VV	16VV1A0431	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
7	VV	16VV1A0433	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
8	VV	16VV1A0438	R1621042	SWITCHING THEORY AND LOGIC DESIGN	D	3
9	VV	16VV1A0456	R1621042	SWITCHING THEORY AND LOGIC DESIGN	C	3
10	VV	16VV1A0459	R1621044	NETWORK ANALYSIS	B	3
11	VV	17VV5A0270	R1621021	ELECTRICAL CIRCUIT ANALYSIS - II	B	3
12	VV	17VV5A1261	R1621053	DIGITAL LOGIC DESIGN	C	3

DATE: 07-04-2018

N. Mohan Rao

CONTROLLER OF EXAMINATIONS