	JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSIRY KAKINADA					
	MTECH II SEM (R16/R13) Regular/Supplementary Examinations JUNE-2017.					
	(VV College only)					
sno	htno	subcode	subname	internals	externals	credits
1	11VV1D0304	RAF0306	Plant Bio - Technology	33	42	1
2	15VV1D1502	H1501	OPTIMIZATION AND RELIABILITY	36	42	3
3	15VV1D1502	H1502	EXPERIMENTAL STRESS ANALYSIS	38	41	3
4	15VV1D1502	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	19	46	3
5	15VV1D1502	H1507	TRIBOLOGY ELECTIVE-III	19	33	3
6	15VV1D1502	H1512	MECHATRONICS ELECTIVE-IV	17	44	3
7	15VV1D1502	H1514	DESIGN PRACTICE LAB	38	54	2
8	15VV1D1502	H2103	FINITE ELEMENT METHOD	30	32	3
9	15VV1D1513	J1501	OPTIMIZATION AND RELIABILITY	35	31	3
10	15VV1D1513	J1502	EXPERIMENTAL STRESS ANALYSIS	34	44	3
11	15VV1D1513	J1503	DESIGN WITH ADVANCED MATERIALS	34	40	3
12	15VV1D1513	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	39	39	3
13	15VV1D1513	J1509	MECHATRONICS ELECTIVEIV	30	42	3
14	15VV1D1513	J1511	DESIGN PRACTICE LAB	33	50	2
15	15VV1D1513	J2103	FINITE ELEMENT METHODS ELECTIVEIII	36	39	3
16	15VV1D5805	H5803	COMPILER DESIGN ELECTIVE-I	12	38	3
17	15VV1D5808	H5801	COMPUTER NETWORKS	14	24	0
18	15VV1D5808	H5803	COMPILER DESIGN ELECTIVE-I	10	39	0
19	15VV1D5813	H5801	COMPUTER NETWORKS	12	24	0
20	16VV1D1501	J1501	OPTIMIZATION AND RELIABILITY	31	34	3
21	16VV1D1501	J1502	EXPERIMENTAL STRESS ANALYSIS	34	32	3
22	16VV1D1501	J1503	DESIGN WITH ADVANCED MATERIALS	28	45	3
23	16VV1D1501	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	36	40	3
24	16VV1D1501	J1509	MECHATRONICS ELECTIVEIV	22	43	3
25	16VV1D1501	J1511	DESIGN PRACTICE LAB	34	55	2
26	16VV1D1501	J2103	FINITE ELEMENT METHODS ELECTIVEIII	37	46	3
27	16VV1D1502	J1501	OPTIMIZATION AND RELIABILITY	37	38	3
28	16VV1D1502	J1502	EXPERIMENTAL STRESS ANALYSIS	38	54	3
29	16VV1D1502	J1503	DESIGN WITH ADVANCED MATERIALS	34	47	3
30	16VV1D1502	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	36	51	3
31	16VV1D1502	J1509	MECHATRONICS ELECTIVEIV	29	50	3

32	16VV1D1502	J1511	DESIGN PRACTICE LAB	37	57	2
33	16VV1D1502	J2103	FINITE ELEMENT METHODS ELECTIVEIII	32	37	3
34	16VV1D1503	J1501	OPTIMIZATION AND RELIABILITY	34	38	3
35	16VV1D1503	J1502	EXPERIMENTAL STRESS ANALYSIS	38	43	3
36	16VV1D1503	J1503	DESIGN WITH ADVANCED MATERIALS	33	48	3
37	16VV1D1503	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	39	51	3
38	16VV1D1503	J1509	MECHATRONICS ELECTIVEIV	24	46	3
39	16VV1D1503	J1511	DESIGN PRACTICE LAB	37	57	2
40	16VV1D1503	J2103	FINITE ELEMENT METHODS ELECTIVEIII	36	40	3
41	16VV1D1504	J1501	OPTIMIZATION AND RELIABILITY	31	32	3
42	16VV1D1504	J1502	EXPERIMENTAL STRESS ANALYSIS	38	50	3
43	16VV1D1504	J1503	DESIGN WITH ADVANCED MATERIALS	34	50	3
44	16VV1D1504	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	36	50	3
45	16VV1D1504	J1509	MECHATRONICS ELECTIVEIV	23	45	3
46	16VV1D1504	J1511	DESIGN PRACTICE LAB	36	54	2
47	16VV1D1504	J2103	FINITE ELEMENT METHODS ELECTIVEIII	37	39	3
48	16VV1D1505	J1501	OPTIMIZATION AND RELIABILITY	26	24	3
49	16VV1D1505	J1502	EXPERIMENTAL STRESS ANALYSIS	33	27	3
50	16VV1D1505	J1503	DESIGN WITH ADVANCED MATERIALS	23	35	3
51	16VV1D1505	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	25	34	3
52	16VV1D1505	J1509	MECHATRONICS ELECTIVEIV	16	34	3
53	16VV1D1505	J1511	DESIGN PRACTICE LAB	35	53	2
54	16VV1D1505	J2103	FINITE ELEMENT METHODS ELECTIVEIII	36	27	3
55	16VV1D1506	J1501	OPTIMIZATION AND RELIABILITY	30	28	3
56	16VV1D1506	J1502	EXPERIMENTAL STRESS ANALYSIS	33	42	3
57	16VV1D1506	J1503	DESIGN WITH ADVANCED MATERIALS	26	48	3
58	16VV1D1506	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	34	41	3
59	16VV1D1506	J1509	MECHATRONICS ELECTIVEIV	19	38	3
60	16VV1D1506	J1511	DESIGN PRACTICE LAB	34	52	2
61	16VV1D1506	J2103	FINITE ELEMENT METHODS ELECTIVEIII	34	30	3
62	16VV1D1507	J1501	OPTIMIZATION AND RELIABILITY	32	24	3
63	16VV1D1507	J1502	EXPERIMENTAL STRESS ANALYSIS	34	29	3
64	16VV1D1507	J1503	DESIGN WITH ADVANCED MATERIALS	22	42	3
65	16VV1D1507	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	34	31	3
66	16VV1D1507	J1509	MECHATRONICS ELECTIVEIV	16	34	3

67	16VV1D1507	J1511	DESIGN PRACTICE LAB	34	51	2
68	16VV1D1507	J2103	FINITE ELEMENT METHODS ELECTIVEIII	31	26	3
69	16VV1D1508	J1501	OPTIMIZATION AND RELIABILITY	35	40	3
70	16VV1D1508	J1502	EXPERIMENTAL STRESS ANALYSIS	36	51	3
71	16VV1D1508	J1503	DESIGN WITH ADVANCED MATERIALS	35	47	3
72	16VV1D1508	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	39	56	3
73	16VV1D1508	J1509	MECHATRONICS ELECTIVEIV	28	44	3
74	16VV1D1508	J1511	DESIGN PRACTICE LAB	34	53	2
75	16VV1D1508	J2103	FINITE ELEMENT METHODS ELECTIVEIII	36	38	3
76	16VV1D1509	J1501	OPTIMIZATION AND RELIABILITY	27	41	3
77	16VV1D1509	J1502	EXPERIMENTAL STRESS ANALYSIS	32	43	3
78	16VV1D1509	J1503	DESIGN WITH ADVANCED MATERIALS	26	42	3
79	16VV1D1509	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	35	37	3
80	16VV1D1509	J1509	MECHATRONICS ELECTIVEIV	19	33	3
81	16VV1D1509	J1511	DESIGN PRACTICE LAB	33	50	2
82	16VV1D1509	J2103	FINITE ELEMENT METHODS ELECTIVEIII	34	31	3
83	16VV1D1510	J1501	OPTIMIZATION AND RELIABILITY	30	36	3
84	16VV1D1510	J1502	EXPERIMENTAL STRESS ANALYSIS	36	36	3
85	16VV1D1510	J1503	DESIGN WITH ADVANCED MATERIALS	30	43	3
86	16VV1D1510	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	39	43	3
87	16VV1D1510	J1509	MECHATRONICS ELECTIVEIV	19	38	3
88	16VV1D1510	J1511	DESIGN PRACTICE LAB	34	55	2
89	16VV1D1510	J2103	FINITE ELEMENT METHODS ELECTIVEIII	36	41	3
90	16VV1D1511	J1501	OPTIMIZATION AND RELIABILITY	32	34	3
91	16VV1D1511	J1502	EXPERIMENTAL STRESS ANALYSIS	37	44	3
92	16VV1D1511	J1503	DESIGN WITH ADVANCED MATERIALS	30	44	3
93	16VV1D1511	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	34	48	3
94	16VV1D1511	J1509	MECHATRONICS ELECTIVEIV	22	41	3
95	16VV1D1511	J1511	DESIGN PRACTICE LAB	36	56	2
96	16VV1D1511	J2103	FINITE ELEMENT METHODS ELECTIVEIII	37	38	3
97	16VV1D1512	J1501	OPTIMIZATION AND RELIABILITY	33	35	3
98	16VV1D1512	J1502	EXPERIMENTAL STRESS ANALYSIS	38	37	3
99	16VV1D1512	J1503	DESIGN WITH ADVANCED MATERIALS	34	47	3
100	16VV1D1512	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	38	44	3
101	16VV1D1512	J1509	MECHATRONICS ELECTIVEIV	28	46	3

102	16VV1D1512	J1511	DESIGN PRACTICE LAB	37	57	2
103	16VV1D1512	J2103	FINITE ELEMENT METHODS ELECTIVEIII	34	43	3
104	16VV1D1513	J1501	OPTIMIZATION AND RELIABILITY	37	34	3
105	16VV1D1513	J1502	EXPERIMENTAL STRESS ANALYSIS	38	47	3
106	16VV1D1513	J1503	DESIGN WITH ADVANCED MATERIALS	28	46	3
107	16VV1D1513	J1505	SIGNAL ANALYSIS AND CONDITION MONITORING ELECTIVEIV	37	40	3
108	16VV1D1513	J1509	MECHATRONICS ELECTIVEIV	27	40	3
109	16VV1D1513	J1511	DESIGN PRACTICE LAB	37	57	2
110	16VV1D1513	J2103	FINITE ELEMENT METHODS ELECTIVEIII	35	48	3
111	16VV1D4501	J4501	ADAPTIVE SIGNAL PROCESSING	38	37	3
112	16VV1D4501	J4502	IMAGE AND VIDEO PROCESSING	28	27	3
113	16VV1D4501	J4503	DETECTION AND ESTIMATION THEORY	38	30	3
114	16VV1D4501	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	30	25	3
115	16VV1D4501	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	35	51	2
116	16VV1D4501	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	26	28	3
117	16VV1D4501	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	30	31	3
118	16VV1D4502	J4501	ADAPTIVE SIGNAL PROCESSING	20	35	3
119	16VV1D4502	J4502	IMAGE AND VIDEO PROCESSING	32	32	3
120	16VV1D4502	J4503	DETECTION AND ESTIMATION THEORY	38	41	3
121	16VV1D4502	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	37	36	3
122	16VV1D4502	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	36	53	2
123	16VV1D4502	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	30	48	3
124	16VV1D4502	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	33	35	3
125	16VV1D4503	J4501	ADAPTIVE SIGNAL PROCESSING	34	37	3
126	16VV1D4503	J4502	IMAGE AND VIDEO PROCESSING	26	36	3
127	16VV1D4503	J4503	DETECTION AND ESTIMATION THEORY	38	42	3
128	16VV1D4503	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	32	38	3
129	16VV1D4503	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	35	54	2
130	16VV1D4503	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	28	52	3
131	16VV1D4503	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	31	39	3
132	16VV1D4504	J4501	ADAPTIVE SIGNAL PROCESSING	39	30	3
133	16VV1D4504	J4502	IMAGE AND VIDEO PROCESSING	29	25	3
134	16VV1D4504	J4503	DETECTION AND ESTIMATION THEORY	37	40	3
135	16VV1D4504	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	24	31	3
136	16VV1D4504	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	37	55	2

137	16VV1D4504	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	30	37	3
138	16VV1D4504	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	31	34	3
139	16VV1D4505	J4501	ADAPTIVE SIGNAL PROCESSING	36	33	3
140	16VV1D4505	J4502	IMAGE AND VIDEO PROCESSING	32	34	3
141	16VV1D4505	J4503	DETECTION AND ESTIMATION THEORY	35	44	3
142	16VV1D4505	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	28	36	3
143	16VV1D4505	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	36	53	2
144	16VV1D4505	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	29	37	3
145	16VV1D4505	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	32	27	3
146	16VV1D4506	J4501	ADAPTIVE SIGNAL PROCESSING	39	36	3
147	16VV1D4506	J4502	IMAGE AND VIDEO PROCESSING	34	31	3
148	16VV1D4506	J4503	DETECTION AND ESTIMATION THEORY	39	44	3
149	16VV1D4506	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	34	32	3
150	16VV1D4506	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	38	56	2
151	16VV1D4506	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	34	44	3
152	16VV1D4506	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	32	35	3
153	16VV1D4507	J4501	ADAPTIVE SIGNAL PROCESSING	35	27	3
154	16VV1D4507	J4502	IMAGE AND VIDEO PROCESSING	27	24	3
155	16VV1D4507	J4503	DETECTION AND ESTIMATION THEORY	33	39	3
156	16VV1D4507	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	30	26	3
157	16VV1D4507	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	36	54	2
158	16VV1D4507	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	28	29	3
159	16VV1D4507	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	30	33	3
160	16VV1D4508	J4501	ADAPTIVE SIGNAL PROCESSING	39	34	3
161	16VV1D4508	J4502	IMAGE AND VIDEO PROCESSING	25	30	3
162	16VV1D4508	J4503	DETECTION AND ESTIMATION THEORY	34	42	3
163	16VV1D4508	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	25	31	3
164	16VV1D4508	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	36	54	2
165	16VV1D4508	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	30	31	3
166	16VV1D4508	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	31	33	3
167	16VV1D4509	J4501	ADAPTIVE SIGNAL PROCESSING	39	33	3
168	16VV1D4509	J4502	IMAGE AND VIDEO PROCESSING	30	32	3
169	16VV1D4509	J4503	DETECTION AND ESTIMATION THEORY	30	40	3
170	16VV1D4509	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	31	44	3
171	16VV1D4509	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	37	55	2

172	16VV1D4509	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	27	29	3
173	16VV1D4509	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	30	29	3
174	16VV1D4510	J4501	ADAPTIVE SIGNAL PROCESSING	36	24	3
175	16VV1D4510	J4502	IMAGE AND VIDEO PROCESSING	24	15	0
176	16VV1D4510	J4503	DETECTION AND ESTIMATION THEORY	33	27	3
177	16VV1D4510	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	23	27	3
178	16VV1D4510	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	34	50	2
179	16VV1D4510	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	26	27	3
180	16VV1D4510	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	30	24	3
181	16VV1D4511	J4501	ADAPTIVE SIGNAL PROCESSING	38	38	3
182	16VV1D4511	J4502	IMAGE AND VIDEO PROCESSING	32	41	3
183	16VV1D4511	J4503	DETECTION AND ESTIMATION THEORY	36	48	3
184	16VV1D4511	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	35	40	3
185	16VV1D4511	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	38	56	2
186	16VV1D4511	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	30	45	3
187	16VV1D4511	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	36	35	3
188	16VV1D4512	J4501	ADAPTIVE SIGNAL PROCESSING	33	28	3
189	16VV1D4512	J4502	IMAGE AND VIDEO PROCESSING	20	30	3
190	16VV1D4512	J4503	DETECTION AND ESTIMATION THEORY	27	32	3
191	16VV1D4512	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	27	34	3
192	16VV1D4512	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	38	56	2
193	16VV1D4512	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	30	30	3
194	16VV1D4512	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	30	28	3
195	16VV1D4514	J4501	ADAPTIVE SIGNAL PROCESSING	39	31	3
196	16VV1D4514	J4502	IMAGE AND VIDEO PROCESSING	26	31	3
197	16VV1D4514	J4503	DETECTION AND ESTIMATION THEORY	33	40	3
198	16VV1D4514	J4508	WIRELESS COMMUNICATIONS AND NETWORKS ELECTIVEIV	30	30	3
199	16VV1D4514	J4509	ADVANCED SIGNAL PROCESSING LABORATORY	38	56	2
200	16VV1D4514	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIV	30	42	3
201	16VV1D4514	J6805	DSP PROCESSORS AND ARCHITECTURES ELECTIVEIV	30	32	3
202	16VV1D5001	J5601	POWER SYSTEM DYNAMICS AND STABILITY	25	32	3
203	16VV1D5001	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	29	44	3
204	16VV1D5001	J5603	REAL TIME CONTROL OF POWER SYSTEMS	28	28	3
205	16VV1D5001	J5604	ADVANCED POWER SYSTEM PROTECTION	31	33	3
206	16VV1D5001	J5606	POWER QUALITY ELECTIVEIII	33	30	3

207	16VV1D5001	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	28	40	3
208	16VV1D5001	J5613	POWER SYSTEMS LABORATORY	32	49	2
209	16VV1D5002	J5601	POWER SYSTEM DYNAMICS AND STABILITY	28	31	3
210	16VV1D5002	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	27	27	3
211	16VV1D5002	J5603	REAL TIME CONTROL OF POWER SYSTEMS	30	37	3
212	16VV1D5002	J5604	ADVANCED POWER SYSTEM PROTECTION	19	44	3
213	16VV1D5002	J5606	POWER QUALITY ELECTIVEIII	34	24	3
214	16VV1D5002	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	28	38	3
215	16VV1D5002	J5613	POWER SYSTEMS LABORATORY	35	54	2
216	16VV1D5003	J5601	POWER SYSTEM DYNAMICS AND STABILITY	25	28	3
217	16VV1D5003	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	31	43	3
218	16VV1D5003	J5603	REAL TIME CONTROL OF POWER SYSTEMS	27	31	3
219	16VV1D5003	J5604	ADVANCED POWER SYSTEM PROTECTION	24	46	3
220	16VV1D5003	J5606	POWER QUALITY ELECTIVEIII	38	40	3
221	16VV1D5003	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	27	39	3
222	16VV1D5003	J5613	POWER SYSTEMS LABORATORY	32	39	2
223	16VV1D5004	J5601	POWER SYSTEM DYNAMICS AND STABILITY	28	42	3
224	16VV1D5004	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	29	47	3
225	16VV1D5004	J5603	REAL TIME CONTROL OF POWER SYSTEMS	32	48	3
226	16VV1D5004	J5604	ADVANCED POWER SYSTEM PROTECTION	26	48	3
227	16VV1D5004	J5606	POWER QUALITY ELECTIVEIII	37	49	3
228	16VV1D5004	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	35	41	3
229	16VV1D5004	J5613	POWER SYSTEMS LABORATORY	35	42	2
230	16VV1D5005	J5601	POWER SYSTEM DYNAMICS AND STABILITY	28	31	3
231	16VV1D5005	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	35	45	3
232	16VV1D5005	J5603	REAL TIME CONTROL OF POWER SYSTEMS	31	38	3
233	16VV1D5005	J5604	ADVANCED POWER SYSTEM PROTECTION	27	41	3
234	16VV1D5005	J5606	POWER QUALITY ELECTIVEIII	31	48	3
235	16VV1D5005	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	30	36	3
236	16VV1D5005	J5613	POWER SYSTEMS LABORATORY	33	47	2
237	16VV1D5006	J5601	POWER SYSTEM DYNAMICS AND STABILITY	23	42	3
238	16VV1D5006	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	32	44	3
239	16VV1D5006	J5603	REAL TIME CONTROL OF POWER SYSTEMS	34	48	3
240	16VV1D5006	J5604	ADVANCED POWER SYSTEM PROTECTION	32	49	3
241	16VV1D5006	J5606	POWER QUALITY ELECTIVEIII	32	37	3

242	16VV1D5006	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	27	38	3
243	16VV1D5006	J5613	POWER SYSTEMS LABORATORY	35	50	2
244	16VV1D5007	J5601	POWER SYSTEM DYNAMICS AND STABILITY	30	37	3
245	16VV1D5007	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	31	37	3
246	16VV1D5007	J5603	REAL TIME CONTROL OF POWER SYSTEMS	36	38	3
247	16VV1D5007	J5604	ADVANCED POWER SYSTEM PROTECTION	32	48	3
248	16VV1D5007	J5606	POWER QUALITY ELECTIVEIII	36	39	3
249	16VV1D5007	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	32	39	3
250	16VV1D5007	J5613	POWER SYSTEMS LABORATORY	33	51	2
251	16VV1D5008	J5601	POWER SYSTEM DYNAMICS AND STABILITY	27	29	3
252	16VV1D5008	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	30	40	3
253	16VV1D5008	J5603	REAL TIME CONTROL OF POWER SYSTEMS	33	28	3
254	16VV1D5008	J5604	ADVANCED POWER SYSTEM PROTECTION	27	31	3
255	16VV1D5008	J5606	POWER QUALITY ELECTIVEIII	35	33	3
256	16VV1D5008	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	27	39	3
257	16VV1D5008	J5613	POWER SYSTEMS LABORATORY	33	45	2
258	16VV1D5009	J5601	POWER SYSTEM DYNAMICS AND STABILITY	22	29	3
259	16VV1D5009	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	31	33	3
260	16VV1D5009	J5603	REAL TIME CONTROL OF POWER SYSTEMS	30	33	3
261	16VV1D5009	J5604	ADVANCED POWER SYSTEM PROTECTION	31	30	3
262	16VV1D5009	J5606	POWER QUALITY ELECTIVEIII	36	25	3
263	16VV1D5009	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	24	37	3
264	16VV1D5009	J5613	POWER SYSTEMS LABORATORY	35	47	2
265	16VV1D5010	J5601	POWER SYSTEM DYNAMICS AND STABILITY	26	31	3
266	16VV1D5010	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	30	35	3
267	16VV1D5010	J5603	REAL TIME CONTROL OF POWER SYSTEMS	32	29	3
268	16VV1D5010	J5604	ADVANCED POWER SYSTEM PROTECTION	27	28	3
269	16VV1D5010	J5606	POWER QUALITY ELECTIVEIII	31	25	3
270	16VV1D5010	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	21	38	3
271	16VV1D5010	J5613	POWER SYSTEMS LABORATORY	33	44	2
272	16VV1D5011	J5601	POWER SYSTEM DYNAMICS AND STABILITY	30	37	3
273	16VV1D5011	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	33	40	3
274	16VV1D5011	J5603	REAL TIME CONTROL OF POWER SYSTEMS	33	39	3
275	16VV1D5011	J5604	ADVANCED POWER SYSTEM PROTECTION	26	38	3
276	16VV1D5011	J5606	POWER QUALITY ELECTIVEIII	34	46	3
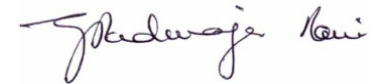
277	16VV1D5011	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	29	40	3
278	16VV1D5011	J5613	POWER SYSTEMS LABORATORY	34	46	2
279	16VV1D5012	J5601	POWER SYSTEM DYNAMICS AND STABILITY	19	39	3
280	16VV1D5012	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	26	39	3
281	16VV1D5012	J5603	REAL TIME CONTROL OF POWER SYSTEMS	31	33	3
282	16VV1D5012	J5604	ADVANCED POWER SYSTEM PROTECTION	17	45	3
283	16VV1D5012	J5606	POWER QUALITY ELECTIVEIII	33	41	3
284	16VV1D5012	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	32	43	3
285	16VV1D5012	J5613	POWER SYSTEMS LABORATORY	31	38	2
286	16VV1D5014	J5601	POWER SYSTEM DYNAMICS AND STABILITY	25	27	3
287	16VV1D5014	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	30	36	3
288	16VV1D5014	J5603	REAL TIME CONTROL OF POWER SYSTEMS	29	24	3
289	16VV1D5014	J5604	ADVANCED POWER SYSTEM PROTECTION	24	31	3
290	16VV1D5014	J5606	POWER QUALITY ELECTIVEIII	32	27	3
291	16VV1D5014	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	20	39	3
292	16VV1D5014	J5613	POWER SYSTEMS LABORATORY	33	51	2
293	16VV1D5015	J5601	POWER SYSTEM DYNAMICS AND STABILITY	29	38	3
294	16VV1D5015	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIVEIV	34	40	3
295	16VV1D5015	J5603	REAL TIME CONTROL OF POWER SYSTEMS	34	37	3
296	16VV1D5015	J5604	ADVANCED POWER SYSTEM PROTECTION	36	45	3
297	16VV1D5015	J5606	POWER QUALITY ELECTIVEIII	38	38	3
298	16VV1D5015	J5609	POWER SYSTEM DEREGULATION ELECTIVEIV	34	42	3
299	16VV1D5015	J5613	POWER SYSTEMS LABORATORY	37	53	2
300	16VV1D5801	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	36	3
301	16VV1D5801	J2503	CYBER SECURITY	22	37	3
302	16VV1D5801	J2510	CLOUD COMPUTING ELECTIVE II	35	35	3
303	16VV1D5801	J4001	ADVANCED UNIX PROGRAMMING	26	43	3
304	16VV1D5801	J4002	BIG DATA ANALYTICS	28	54	3
305	16VV1D5801	J5801	COMPUTER NETWORKS	29	39	3
306	16VV1D5801	J5803	CSE LAB 2	32	50	2
307	16VV1D5802	J0502	SOFTWARE ENGINEERING ELECTIVE I	39	37	3
308	16VV1D5802	J2503	CYBER SECURITY	37	44	3
309	16VV1D5802	J2510	CLOUD COMPUTING ELECTIVE II	36	40	3
310	16VV1D5802	J4001	ADVANCED UNIX PROGRAMMING	37	48	3
311	16VV1D5802	J4002	BIG DATA ANALYTICS	37	54	3

312	16VV1D5802	J5801	COMPUTER NETWORKS	40	41	3
313	16VV1D5802	J5803	CSE LAB 2	34	54	2
314	16VV1D5803	J0502	SOFTWARE ENGINEERING ELECTIVE I	39	32	3
315	16VV1D5803	J2503	CYBER SECURITY	30	38	3
316	16VV1D5803	J2510	CLOUD COMPUTING ELECTIVE II	32	41	3
317	16VV1D5803	J4001	ADVANCED UNIX PROGRAMMING	25	44	3
318	16VV1D5803	J4002	BIG DATA ANALYTICS	19	50	3
319	16VV1D5803	J5801	COMPUTER NETWORKS	38	41	3
320	16VV1D5803	J5803	CSE LAB 2	30	51	2
321	16VV1D5804	J0502	SOFTWARE ENGINEERING ELECTIVE I	33	26	3
322	16VV1D5804	J2503	CYBER SECURITY	22	37	3
323	16VV1D5804	J2510	CLOUD COMPUTING ELECTIVE II	29	31	3
324	16VV1D5804	J4001	ADVANCED UNIX PROGRAMMING	20	38	3
325	16VV1D5804	J4002	BIG DATA ANALYTICS	19	41	3
326	16VV1D5804	J5801	COMPUTER NETWORKS	33	33	3
327	16VV1D5804	J5803	CSE LAB 2	28	38	2
328	16VV1D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	34	29	3
329	16VV1D5805	J2503	CYBER SECURITY	17	33	3
330	16VV1D5805	J2510	CLOUD COMPUTING ELECTIVE II	29	28	3
331	16VV1D5805	J4001	ADVANCED UNIX PROGRAMMING	23	33	3
332	16VV1D5805	J4002	BIG DATA ANALYTICS	24	38	3
333	16VV1D5805	J5801	COMPUTER NETWORKS	31	31	3
334	16VV1D5805	J5803	CSE LAB 2	32	55	2
335	16VV1D5806	J0502	SOFTWARE ENGINEERING ELECTIVE I	32	38	3
336	16VV1D5806	J2503	CYBER SECURITY	16	34	3
337	16VV1D5806	J2510	CLOUD COMPUTING ELECTIVE II	29	32	3
338	16VV1D5806	J4001	ADVANCED UNIX PROGRAMMING	21	40	3
339	16VV1D5806	J4002	BIG DATA ANALYTICS	28	50	3
340	16VV1D5806	J5801	COMPUTER NETWORKS	27	38	3
341	16VV1D5806	J5803	CSE LAB 2	33	34	2
342	16VV1D5807	J0502	SOFTWARE ENGINEERING ELECTIVE I	39	36	3
343	16VV1D5807	J2503	CYBER SECURITY	24	40	3
344	16VV1D5807	J2510	CLOUD COMPUTING ELECTIVE II	31	37	3
345	16VV1D5807	J4001	ADVANCED UNIX PROGRAMMING	25	45	3
346	16VV1D5807	J4002	BIG DATA ANALYTICS	23	48	3

347	16VV1D5807	J5801	COMPUTER NETWORKS	34	42	3
348	16VV1D5807	J5803	CSE LAB 2	34	54	2
349	16VV1D5808	J0502	SOFTWARE ENGINEERING ELECTIVE I	34	42	3
350	16VV1D5808	J2503	CYBER SECURITY	22	38	3
351	16VV1D5808	J2510	CLOUD COMPUTING ELECTIVE II	27	31	3
352	16VV1D5808	J4001	ADVANCED UNIX PROGRAMMING	31	44	3
353	16VV1D5808	J4002	BIG DATA ANALYTICS	20	57	3
354	16VV1D5808	J5801	COMPUTER NETWORKS	33	39	3
355	16VV1D5808	J5803	CSE LAB 2	26	46	2
356	16VV1D5809	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	41	3
357	16VV1D5809	J2503	CYBER SECURITY	33	41	3
358	16VV1D5809	J2510	CLOUD COMPUTING ELECTIVE II	31	34	3
359	16VV1D5809	J4001	ADVANCED UNIX PROGRAMMING	33	41	3
360	16VV1D5809	J4002	BIG DATA ANALYTICS	34	43	3
361	16VV1D5809	J5801	COMPUTER NETWORKS	39	38	3
362	16VV1D5809	J5803	CSE LAB 2	33	50	2
363	16VV1D5810	J0502	SOFTWARE ENGINEERING ELECTIVE I	38	30	3
364	16VV1D5810	J2503	CYBER SECURITY	19	35	3
365	16VV1D5810	J2510	CLOUD COMPUTING ELECTIVE II	28	31	3
366	16VV1D5810	J4001	ADVANCED UNIX PROGRAMMING	19	31	3
367	16VV1D5810	J4002	BIG DATA ANALYTICS	22	39	3
368	16VV1D5810	J5801	COMPUTER NETWORKS	28	35	3
369	16VV1D5810	J5803	CSE LAB 2	34	30	2
370	16VV1D5811	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	37	3
371	16VV1D5811	J2503	CYBER SECURITY	17	34	3
372	16VV1D5811	J2510	CLOUD COMPUTING ELECTIVE II	24	26	3
373	16VV1D5811	J4001	ADVANCED UNIX PROGRAMMING	30	39	3
374	16VV1D5811	J4002	BIG DATA ANALYTICS	23	45	3
375	16VV1D5811	J5801	COMPUTER NETWORKS	21	33	3
376	16VV1D5811	J5803	CSE LAB 2	29	44	2
377	16VV1D5812	J0502	SOFTWARE ENGINEERING ELECTIVE I	24	30	3
378	16VV1D5812	J2503	CYBER SECURITY	12	29	0
379	16VV1D5812	J2510	CLOUD COMPUTING ELECTIVE II	23	33	3
380	16VV1D5812	J4001	ADVANCED UNIX PROGRAMMING	6	10	0
381	16VV1D5812	J4002	BIG DATA ANALYTICS	6	31	0

382	16VV1D5812	J5801	COMPUTER NETWORKS	25	31	3
383	16VV1D5812	J5803	CSE LAB 2	24	30	2
384	16VV1D5814	J0502	SOFTWARE ENGINEERING ELECTIVE I	35	36	3
385	16VV1D5814	J2503	CYBER SECURITY	14	36	3
386	16VV1D5814	J2510	CLOUD COMPUTING ELECTIVE II	28	36	3
387	16VV1D5814	J4001	ADVANCED UNIX PROGRAMMING	15	40	3
388	16VV1D5814	J4002	BIG DATA ANALYTICS	10	40	3
389	16VV1D5814	J5801	COMPUTER NETWORKS	30	37	3
390	16VV1D5814	J5803	CSE LAB 2	24	30	2

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CONTROLLER OF EXAMINATIONS(PG)