

2008 & 2009 Batches

UNIVERSITY COLLEGE OF ENGINEERING VIZIANAGARAM
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

III B.Tech I Semester (R07) Supplementary Examinations Time Table, MAY-2016

Time: 10.00 AM to 01.00PM

DATE / DAY	ELECTRICAL & ELECTRONICS ENGINEERING	MECHANICAL ENGINEERING	ELECTRONICS COMMUNICATION ENGINEERING	COMPUTER SCIENCE AND ENGINEERING	INFORMATION TECHNOLOGY
03-05-2016 (TUESDAY)	Computer System Organization	Automobile Engineering	Computer Organization	Formal Languages & Automata Theory	Automata and Compiler Design
05-05-2016 (THURSDAY)	Electrical Measurements	Managerial Economics & Financial Analysis	Managerial Economics & Financial Analysis	Data Communication System	Data Communication System
07-05-2016 (SATURDAY)	Power Systems-II	Dynamics of Machinery	Linear IC Applications	Software Testing Methodologies	Software Testing Methodologies
10-05-2016 (TUESDAY)	Power Electronics	Machine Tools	Digital IC Applications	Computer Graphics	Computer Graphics
12-05-2016 (THURSDAY)	Electrical Machines -III	Design of Machine Members-I	Antennas and Wave Propagation	Microprocessor & Interfacing	Microprocessor & Interfacing
14-05-2016 (SATURDAY)	Linear System Analysis	Heat Transfer	Digital Communications	Design & Analysis of Algorithm	Distributed Databases



PRINCIPAL