



University College of Engineering Vizianagaram
(Autonomous)
JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKIANADA

The following is the provisional list of candidates appeared for **I M.Tech II Semester [R17] Regular** held in **JULY 2018**

Advanced Power System

Passed in ALL SUBJECTS

17VV1D5001 17VV1D5002 17VV1D5003 17VV1D5005 17VV1D5006 17VV1D5008

17VV1D5009 17VV1D5010 17VV1D5012

Failed in Power System Dynamics and Stability

17VV1D5011

Failed in Flexible AC Transmission Systems

---NILL---

Failed in Real Time Control of Power Systems

---NILL---

Failed in Advanced Power System Protection

17VV1D5007

Failed in Power Quality

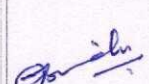
17VV1D5004

Failed in Power System Deregulation


---NILL---

Failed in Power Systems Laboratory

---NILL---


OIE


VICE PRINCIPAL


PRINCIPAL

NOTE: Those who wish to apply for Re-Valuation, Submit your application on or before 08/11/2018

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VIZIANAGARAM



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Thermal Engineering

Passed in ALL SUBJECTS

17VV1D2101	17VV1D2102	17VV1D2103	17VV1D2104	17VV1D2105	17VV1D2106
17VV1D2107	17VV1D2108	17VV1D2109	17VV1D2110	17VV1D2111	17VV1D2112
17VV1D2113	17VV1D2114				

Failed in Fuels, Combustion & Environment

---NILL---

Failed in Energy Management

---NILL---

Failed in Finite Element Method

---NILL---

Failed in Computational Fluid Dynamics

---NILL---

Failed in Thermal and Nuclear Power Plants

---NILL---

Failed in Thermal Measurements and Process Controls

---NILL---

Failed in Thermal Systems Design Lab

---NILL---


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Systems and Signal Processing

Passed in ALL SUBJECTS

17VV1D4502	17VV1D4503	17VV1D4504	17VV1D4505	17VV1D4506	17VV1D4507
17VV1D4508	17VV1D4509	17VV1D4510	17VV1D4511	17VV1D4512	17VV1D4513

Failed in Adaptive Signal Processing

---NILL---

Failed in Image & Video Processing

---NILL---

Failed in Detection and Estimation Theory

---NILL---

Failed in DSP Processors and Architectures

---NILL---

Failed in Embedded System Design


---NILL---

Failed in Wireless Communications & Networks

---NILL---

Failed in Signal Processing Laboratory

---NILL---


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The following is the provisional list of candidates appeared for I M.Tech II Semeter [R17] Regular held in
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Computer Science & Engineering

Passed in ALL SUBJECTS

17VV1D5809 17VV1D5811

Failed in Applied Cryptography

---NIL---

Failed in Network Programming

17VV1D5813

Failed in Data Warehousing And Data Mining

17VV1D5801 17VV1D5805 17VV1D5806 17VV1D5812 17VV1D5813 17VV1D5814

Failed in Big Data Analytics

17VV1D5813

Failed in Web Technologies

---NIL---

Failed in Cloud Computing

---NIL---

Failed in CSE Lab 2

17VV1D5801 17VV1D5802 17VV1D5803 17VV1D5804 17VV1D5805 17VV1D5806

17VV1D5807 17VV1D5812 17VV1D5813 17VV1D5814


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The following is the provisional list of candidates appeared for **I M.Tech II Semester [R17] Regular** held in **JULY 2018**

Material Science and Technology

Passed in ALL SUBJECTS

17VV1D2401	17VV1D2402	17VV1D2403	17VV1D2404	17VV1D2405	17VV1D2406
17VV1D2407	17VV1D2408	17VV1D2409	17VV1D2410	17VV1D2411	17VV1D2412

Failed in Metallurgical Failure Analysis

---NIL---

Failed in Metal Joining and Forming

---NIL---

Failed in Advanced Powder Metallurgy

---NIL---

Failed in Finite Element Techniques in Materials Processing

---NIL---

Failed in Nanomaterials

---NIL---

Failed in Corrosion and Surface Engineering

---NIL---

Failed in Finite Element Techniques in Materials Processing Lab

---NIL---


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